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PATENT  
P16579  
0077.0026

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)	Examiner: Not Yet Assigned
P.L. Connor	)	
Serial No.: 10/687,944	)	
Filed: October 17, 2003	)	Art Unit: 2182
For: METHOD, APPARATUS,	)	
SYSTEM, AND ARTICLE OF	)	
MANUFACTURE FOR	)	
GENERATING INTERRUPTS	)	

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

In accordance with 37 CFR §§ 1.56, 1.97, and 1.98, enclosed please find Form PTO-1449 listing the attached references which might be deemed material to the examination of the above-identified application. Applicants submit that no fee is needed nor should be charged with the submittal of this Information Disclosure Statement (IDS) because this IDS is being submitted under 1.97(b) in that the IDS is being filed before the mailing date of a first Office Action on the merits or within three months of the filing date of a national application other than a continued prosecution application under 1.53(d). Applicants submit that a first Office Action has not been received in the above referenced case.

Nonetheless, should any fee be deemed necessary, please charge any insufficiency or credit any overpayment to Deposit Account No. 50-0585. A duplicate of this letter is enclosed for fee processing.

Respectfully submitted,



Rabindranath Dutta

Dated: June 21, 2004

Registration No. 51,010

KONRAD RAYNES & VICTOR, LLP

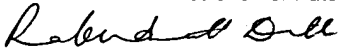
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Rabindranath Dutta

6/21/04  
(Date)

<div style="text-align: center;">  <p>Form 1449/PTO</p> <p><b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b></p> <p>(use as many sheets as necessary)</p> </div>				<p><i>Complete if Known</i></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Application Number</td> <td>10/687,944</td> </tr> <tr> <td>Filing Date</td> <td>October 17, 2003</td> </tr> <tr> <td>First Named Inventor</td> <td>P.L. Connor</td> </tr> <tr> <td>Art Unit</td> <td>2182</td> </tr> <tr> <td>Examiner Name</td> <td>Not Yet Assigned</td> </tr> <tr> <td>Attorney Docket Number</td> <td>P16579</td> </tr> </table>		Application Number	10/687,944	Filing Date	October 17, 2003	First Named Inventor	P.L. Connor	Art Unit	2182	Examiner Name	Not Yet Assigned	Attorney Docket Number	P16579
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U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number- Kind Code (if known)			
		US-2003/0110281	06/12/2003	Minnick et al.	
		US-			

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
		Country Code- Number- Kind Code				

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T
		"Method for Operation-Based Adaptive Transmit Interrupt Moderation", <i>IP.com</i> , 25066, DISCLOSED ANONYMOUSLY.	
		"Select Interrupt Assertion of Egress Events", <i>IP.com</i> , 23175, DISCLOSED ANONYMOUSLY.	
		U.S. Patent Application Serial No. 09/409,626, filed September 30, 1999, with Intel Docket No. P7089, entitled "Reducing Network Interrupts", invented by R. D. DUNLAP, G. D. CUMMINGS, P. L. CONNOR, G. G. LI, & J. A. RONCIK.	
		U.S. Patent Application Serial No. 09/955,370, filed September 17, 2001, with Intel Docket No. P12273, entitled "METHODS AND APPARATUS FOR REDUCING RECEIVE INTERRUPTS VIA PACED INGRESS INDICATION", invented by P. J. LUHMANN & P. L. CONNOR.	
		U.S. Patent Application Serial No. 09/967,300, filed September 27, 2001, with Intel Docket No. P11381, entitled "Apparatus and Method for Packet Ingress Interrupt Moderation", invented by P. L. CONNOR.	
		U.S. Patent Application Serial No. 10/055,922, filed January 28, 2002, with Intel Docket No. P12819, entitled "Ingress Throttling Via Adaptive Interrupt Delay Scheduling", invented by P. L. CONNOR, D. GAUR, E. MANN, G. TSAO, & M. GIBSON.	
		U.S. Patent Application Serial No. 10/326,740, filed December 20, 2002, with Intel Docket No. P14972, entitled "Method, System, and Article of Manufacture for Adjusting Interrupt Levels", invented by P. L. CONNOR, E. K. MANN, H. T. TRAN, P. GOVINDARAJAN, J. P. JACOBS, D. M. DURHAM, G. D. GUMANOW, & C. Y. CHIU.	
		U.S. Patent Application Serial No. 10/464,512, filed June 19, 2003, with Intel Docket No. P16544, entitled "Bandwidth Conserving Protocol for Command-Response Bus System", invented by P. L. CONNOR.	

Examiner Signature		Date Considered	
<p>EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.</p>			